InKS, a programming model to decouple performance from semantics in simulation codes
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To cite this version:
Olivier Aumage, Julien Bigot, Ksander Ejjaaouani, Michel Mehrenberger. InKS, a programming model to decouple performance from semantics in simulation codes. 2017. cea-01493075

HAL Id: cea-01493075
https://hal-cea.archives-ouvertes.fr/cea-01493075
Submitted on 20 Mar 2017

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Abstract—Existing programming models lead to a tight inter-leaving of semantics and computer optimization concerns in high-performance simulation codes. With the increasing complexity and heterogeneity of supercomputers this requires scientists to become experts in both the simulated domain and the optimization process and makes the code difficult to maintain and port to new architectures. This report proposes InKS, a programming model that aims to improve the situation by decoupling semantics and optimizations in code so as to ease the collaboration between domain scientists and expert of high-performance optimizations. We define the InKS language that enables developers to describe the semantic of a simulation code with no concern for performance. We describe the implementation of a compiler able to automatically execute this InKS code without making any explicit execution choice. We also describe a method to manually specify these choices to reach high-performance. Our preliminary evaluation on a 3D heat equation solver demonstrates the feasibility of the automatic approach as well as the ability to specify complex optimizations while not altering the semantic part. It shows promising performance where two distinct specifications of optimization choices in InKS offer similar performance as existing hand-tailored versions of the solver.

Keywords—HPC, programming model, separation of concerns.
the loops iterate in a pretty straightforward order whereas those one implementation to the other. In the example from Listing 1 computations inside the loops on the other hand differs from part of the loops that gives values to indexes and schedules and is very similar from one version of the code to the other that operate on the arrays content (lines 11 to 18 of Listing 1) loops. The content of these loops is made of computations code version; it is an optimization choice.

For all versions of the code and is part of the semantic but the (in the 3D space + 1D time coordinate system) is the same at any given time depending on the space coordinate. To many distinct time-steps are however stored inside each array operation. One array contains values from odd time-steps only make it possible to access one array or another using a modulo arrays are stored inside an array of arrays (Listing 2) that A0 requires a different memory storage. The step. In the cache oblivious version, the time-blocking aspect time-step mixed with values being computed for the next time contains values remaining from the previous time-step mixed with values being computed for the next time step. In the cache oblivious version, the time-blocking aspect requires a different memory storage. The A0 and Anext arrays are stored inside an array of arrays (Listing 2) that make it possible to access one array or another using a modulo operation. One array contains values from odd time-steps only while the other contains values from even time-steps only. Many distinct time-steps are however stored inside each array at any given time depending on the space coordinate. To summarize, the set of values computed along the simulation (in the 3D space + 1D time coordinate system) is the same for all versions of the code and is part of the semantic but the mapping of these values in memory differs depending on the code version; it is an optimization choice.

All studied implementations of the heat equation solver use loops. The content of these loops is made of computations that operate on the arrays content (lines 11 to 18 of Listing 1) and is very similar from one version of the code to the other apart from indexing issues previously discussed. The control part of the loops that gives values to indexes and schedules computations inside the loops on the other hand differs from one implementation to the other. In the example from Listing 1 the loops iterate in a pretty straightforward order whereas those from Listing 3 used for cache blocking are more complex. In the cache oblivious examples, the loops are different again and the iterations depend on parameters of the recursive function calls. These schedules are different to improve cache behavior, but all respect ordering constraints, namely that any value has to be written to memory before it is first read and that its storage space in memory must not be reused for another value before it is last read. The content of the loops as well as these ordering constraints thus constitute the semantic part while the choice of a specific schedule that respects the constraints is an optimization choice.

All the examples end with the values from the target time-step in the A0 array. Among all the computed values, we can consider this subset as the result of the program. This also constitutes a part of the application semantic.

To summarize, we have identified four concerns that form the semantics of the 3D heat equation solver: the values that exist during the execution, the computations done inside the loops, the constraints on computation order and the target result. We also have identified two types of execution choices: the memory mapping of data and the specific scheduling to use. More optimization choices could appear, for example in distributed memory parallel versions of the code where choices related to the distribution of data on nodes and communications would have to be made. If the semantic part contains enough information to derive a sequential version of the code however, there is no reason for a parallel version to require more information.

All in all, these examples written in C interleave semantics and optimization choices. A complete new code is written to demonstrate each distinct optimization. In order to ease the collaboration between specialists of the simulated domain and specialists of computer optimizations, one would like a programming model that clearly separates these two aspects. Even if this is an important goal, this should however not come at the cost of a much increased complexity for the specification of the semantic and it should remain possible to test this semantic without having to specify complex optimization choices. It should also be possible to specify any optimization and the specification of these choices should not be much more complex than it currently is in existing imperative languages. It should be possible to express a wide range of different problems in this language so as to cover as many simulation domains as possible and to include this inside another program written in existing languages such as C or Fortran for example to make the progressive adoption of the language possible. The following section presents and discusses existing programming languages with related goals.

```c
#define Index3D(_nx, _ny, i, j, k) \
的操作数类型 (lines 11 to 18 of Listing 1) and is very similar from one version of the code to the other apart from indexing issues previously discussed. The control part of the loops that gives values to indexes and schedules computations inside the loops on the other hand differs from one implementation to the other. In the example from Listing 1 the loops iterate in a pretty straightforward order whereas those from Listing 3 used for cache blocking are more complex. In
```
Multiple approaches have been proposed to ease the development of high-performance simulation codes. Some approaches aim to ease the implementation of execution choices. For example, OpenMP [6] eases writing shared memory parallel code with features such as the specification of independent loop iterations that can be executed in parallel. Partitioned global address space (PGAS) languages such as Co-Array Fortran [14], UPC [11], X10 [7] or XcalableMP [13] simplify the handling of distributed memory by presenting it to the developer as a single global space. These tools can lead to efficient code and make the implementation of some optimization (typically those related to parallelization) much easier. However, they only cover some optimizations and are based on sequential languages similar to C that keep optimization choices tightly coupled with semantic.

Others approaches like Kokkos [10] separate some execution choices but leave others interleaved with semantics. Kokkos is a C++ library that offers multidimensional arrays for which the memory mapping and iteration approach can be separately chosen, either automatically or as a manual parameter. Other execution choices do however remain interleaved with the semantic in the code. For example, a matrix operation such as \( R = A \ast B + C \) can also be written as two steps: \( R_1 = A \ast B; R = R_1 + C \). Both are equivalent from a semantic point of view, but the first notation will lead to a single loop nest while the second one will lead to two. Thus, while some optimization choice are separated from the semantics, other remain interleaved with it.

Similarly, approaches based on task scheduling such as offered by StarPU [2] or Legion [4] let a runtime make choices regarding the best execution order for a set of tasks given dependency constraints. Making these choices at runtime does however mean they have an impact on execution time and that the tasks should be large enough to mask the overhead related to the use of the runtime. The choice of whether to group computations in a single task or to split them in two tasks is thus an optimization choice that is mixed with semantics.

Finally, approaches based on domain specific languages (DSLs) such as those offered by PATUS [8], PIPES [12], Listz [9] or Nabla [5] offer to describe the semantic using the DSL not taking optimization choices into account. Performance is handled by the DSL compiler that is able to generate efficient code because it embeds knowledge about the specific domain the DSL targets. This makes it possible to separate concerns quite well while offering good performance. These approaches do however restrict the optimizations that can be easily implemented. Since optimizations are embedded in the compiler, one cannot implement a specific optimization without modifying the compiler, which is complex.

The authors know of no single model that enables to completely separate semantics from optimization while keeping the ability to easily implement any optimization choice. The following section thus presents our proposition of a model with that aim: InKS.

### IV. The InKS Programming Model

Let us now define the InKS programming model intended to address the issues identified in Section [III]. In order to separate semantics from optimization concerns, we must first define a language that supports the description of the semantic with no concern for execution choices. We call it the InKS language. In order to support testing InKS code without having to deal with optimization choices, we must define an automated execution strategy and we must define another language to support the expression of specific execution choices. In order to support mixing InKS code with more traditional programming models in a single application, the result of both approaches should lead to a code that can be inserted in an existing program.

One can first notice that languages that support the specification of most potential execution choices already exist in the form of traditional imperative languages such as C, Fortran or C++. We therefore choose to use C as the language for the definition of execution choices which has the additional advantage of being well known and not requiring any additional knowledge for optimization of InKS code.

In order to make the use of the semantic part easy in this process, we choose to also base the InKS language used to define the semantic part on C. Amongst the four elements of semantics identified in Section [III] the computations done inside the loops can directly be expressed in C, but the lack of a notion of multi-dimensional array force to choose a memory mapping for those. We therefore choose to use C++ instead where dedicated classes can be used to support this abstraction. To make the semantic code valid C++ that can be directly included in the optimization part, we choose to express other aspects of the semantic using `#pragma` annotations.

The most widely supported standard for inter-language calls is C and to support the inclusion of InKS code inside a larger application, we specify the interface offered by an InKS kernel as a C interface. This can easily be achieved when specifying optimizations manually since those are written in C/C++. We choose to support automatic execution with a source-to-source compiler that generates C++ for the same reason.

The remaining of the section defines the InKS language and provides a rough sketch of proof that this language provides the complete semantics of a program: i.e. enough information to execute it. It then describes the implementation of the InKS compiler for automatic execution and the approach for manual execution choices specification.

#### A. The InKS language

In order to introduce the InKS language, let us rely on an InKS implementation of the 3D heat equation solver presented in Listing [4]. The InKS language supports the specification of the four semantic elements identified in Section [III] the data that exists during execution, the computations done inside the loops, the constraints on computation order and the target result. As previously specified, computations are specified using the host C++ language while other aspects are specified using directives of the form `#pragma inks`. 
#define nx 1024
#define ny 1024
#define nz 1024
#defineimesteps 3

#pragma inks declare double Heat(4)

#pragma inks Bound in Heat(x, y, z, t-1) \
out Heat(x, y, z, t)
#pragma inks Bound definition x(0); \ 
y(0, ny); z(0, nz); t(1, timesteps)
#pragma inks Bound definition x(nx-1); \ 
y(0, ny); z(0, nz); t(1, timesteps)
#pragma inks Bound definition x(0, nx); \ 
y(0, ny); z(0); t(1, timesteps)
#pragma inks Bound definition x(0, nx); \ 
y(0, ny); z(nz-1); t(1, timesteps)
#pragma inks Bound in Heat(x, y, z, t-1) \
#pragma inks declare double Heat(4)

void Bound(T& Heat, int x, int y, int z, int t){
  Heat(x, y, z, t) = Heat(x, y, z, t-1);
}

#pragma inks Inner in Heat(x, y, z, t-1) \
#pragma inks declare double Heat(4)

void Inner(T& Heat, int x, int y, int z, int t){
  Heat(x, y, z, t) = Heat(x+1, y, z, t-1) + \ 
  Heat(x, y, z, t-1); Heat(x-1, y, z, t-1) + \ 
  Heat(x, y, z, t-1); Heat(x, y+1, z, t-1) + \ 
  Heat(x, y, z, t-1); Heat(x, y-1, z, t-1) + \ 
  Heat(x, y, z, t-1); Heat(x, y, z+1, t-1) + \ 
  Heat(x, y, z, t-1); Heat(x, y, z-1, t-1); \ 
  (fac*fac);
}

#pragma inks StencilInit in null \
#pragma inks declare double Heat(4)

void StencilInit(T& Heat){
  read_file(Heat);
}

#pragma inks StencilInit in null \
#pragma inks declare double Heat(4)

#define nz 1024
#define ny 1024
#define nx 1024

template <typename T>
out Heat(*, *, *, 0)
#pragma inks StencilInit in null \
#pragma inks declare double Heat(4)

#pragma inks target Heat(*, *, *, timestep-1)

Listing 4. InKS implementation of the heat equation in 3D

Data is represented using logically infinite multidimensional arrays that have no direct link with memory. Arrays are declared using the #pragma inks declare notation. These are dynamic single assignment arrays where each coordinate from its domain is used in input or created in output of a kernel. For example, lines 8 to 12 of Listing 4 specify that the Inner kernel accesses 7 points as input and generate the value of one as output. The dependencies of the StencilInit kernel in lines 24 and 25 express the lack of input using the keyword null and the generation of every values in the first three dimensions using the star (*).

Constraints on execution order are based on data dependencies of the kernel. Dependencies are defined using the #pragma inks <kernel> in ... out ... notation. They use the coordinate system to declare data that is used in input or created in output of a kernel. For example, the evolution domain in lines 13 and 15 of Listing 4 prevents the values at t = 0 from being considered. The StencilInit kernel on the other hand uses no coordinate and does thus not need an evolution domain.

The target result of the program is a subset of the declared data. This is specified using the #pragma inks target notation. For example in line 31 of Listing 4 the set of all values at the last iteration is selected by using the star notation for space coordinate and a single value for the time coordinate.

These elements constitute the InKS language and support the expression of all elements of semantics identified in Section II. Let us now roughly demonstrate that this constitute the whole semantic of a program; i.e. that the language carries enough information to execute it.

B. InKS language completeness analysis

The informations available in the InKS language are:
- the set of logical arrays \( A = \{a_1, ..., a_n\} \)
- the dimension \( \dim(A) \in \mathbb{N} \) of each logical array \( a \in A \)
- the set of kernels \( K = \{k_1, ..., k_n\} \)
- the definition domain \( \dom(k) \) of each kernel \( k \in K \) with its dimension \( n = \dim(k) \), \( \dom(k) \subseteq \mathbb{Z}^n \)
- the dependencies (inputs \( I_k \) and outputs \( O_k \)) of each kernel \( k \) in the logical arrays, the set of targets \( T = \{t_1, ..., t_n\} \).

We call kernel instance the association of a kernel \( k \) with a coordinate from its domain \( \dom(k) \subseteq \mathbb{Z}^\dim(k) \) and we denote \( K \) the set of all kernel instances.
distinct kernel instances should always be empty. On the other
That is, the intersection of the output relation applied to two
formed as the union of all kernel dependencies that map kernel
I and outputs $O_k$ dependencies of a kernel $k$ map each instance of this kernel to the data it reads or writes. We denote $I$ and $O$ the general input and output relations formed as the union of all kernel dependencies that map kernel instances to data.

$$I_k, O_k : K \mapsto \mathcal{P}(D)$$

$$I = \bigcup_{k \in K} I_k, O = \bigcup_{k \in K} O_k$$

In order for the program to be well formed, a given data instance should only be produced by a single kernel instance. That is, the intersection of the output relation applied to two distinct kernel instances should always be empty. On the other hand, multiple kernel instances can take the same data instance as input.

$$\forall k_1, k_2 \in K, k_1 \neq k_2 \Rightarrow O(k_1) \cap O(k_2) = \emptyset$$

Each target $t$ is a subset of the data that the computation must generate, we denote $T$ the union of all targets.

$$t \in T : t \subseteq D$$

$$T \subseteq D = \bigcup_{t \in T} t$$

The goal of the compiler is to generate a code that computes $T$. Consequently, for the program to be well formed, $T$ must be the output of some kernel. We denote $K_T$ this set of kernel instances that generate the target data.

$$K_T \subseteq K, \left( \bigcup_{k \in K_T} O(k) \subseteq T \right) \land \left( k_1 \in K_T \Rightarrow O(k) \cap T \neq \emptyset \right)$$

In addition, a kernel instance $k_1$ must be computed before an instance $k_2$, denoted $k_1 \prec k_2$ if $k_1$ generates data as output that $k_2$ accesses as input.

$$\forall k_1, k_2 \in K, O(k_1) \cap I(k_2) \neq \emptyset \iff k_1 \prec k_2$$

For the program to be well defined, there must be no loop in its dependencies and we can define $\leq$ the transitive closure of $\prec$ that constitute a partial order relation on $K$. The set of kernel instances $K_x$ that must be executed to generate $T$ is the set of all instances that come before at least one instance in $K_T$.

$$K_x = \{ k \in K | \exists k_t \in K_T, k \leq k_t \}$$

The data $D_x$ that will have to be allocated at some point for the execution of all $K_x$ is the data that is the input or output of at least one such kernel instance. For the program to be valid, all the data instances accessed as input of an executed kernel instance must be part of the output of another kernel instance, one can therefore define the allocated data from the output only.

$$D_x = \bigcup_{k \in K_x} I(k) \cup O(k) = \bigcup_{k \in K_x} O(k)$$

One can augment the executed kernel instances with instances representing the allocation $ka(d)$ and deallocation $kd(d)$ of each data instance $d$. The order relation can also be defined on these kernel instances by taking into account that a data instance must be allocated before it written and deallocated after it is last accessed.

$$K_x^+ = K_x \cup \bigcup_{d \in D_x} \{ ka(d), kd(d) \}$$

$$\forall (k, d) \in K_x \times D_x, d \in I(k) \iff ka(d) \prec k$$

$$\forall (k, d) \in K_x \times D_x, d \in O(k) \iff k \prec kd(d)$$

To summarize, the InKS language provides enough information to construct the $K_x^+$ set that specifies all memory allocations and deallocations and kernel instances to execute with a partial order relation that determines the constraint on the scheduling of these operations. Let us now describes the actual implementation of this process in the InKS compiler.

C. Source-to-source compiler

A naive approach for the implementation of the scheduling could consist in enumerating the set of operations to execute and to generate a source code that does each of them in a valid order. The complexity of such an approach–both in time and space– as well as the generated code size would however be at least linear with the number of instances, i.e. the problem size. In fact, if the kernels implemented by the user operate at a fine enough grain, the compilation could easily require more resources than the execution itself. This situation would not be acceptable and we have to rely on a more compact representation of the problem and generated code.

The polyhedral theory offers a solution in the case where each domain can be described in terms of polyhedron. Given the language introduced in Section [IV] this is the case since we currently restrict ourselves to unions of rectangle regions. We have therefore chosen to base our implementation on the isl [16] library that supports the manipulation of sets and relations of integer points bounded by linear constraints.


1  #pragma inks Inner \  
2        in H(x, y, z, t−1) \  
3        out H(x, y, z, t)  
4  template <typename T>  
5  void Inner(T H, int x, int y, int z, int t)  
6  {  
7        //...  
8  }

Listing 5. Example of input and output dependencies in InKS code

InKS provides functions to allocate multidimensional spaces in which one can create multidimensional sets. These sets–potentially parametric–contain every integer point between the set bounds. One can also manipulate these sets with different operations (i.e. projection, intersection, union, etc.). One also can create relations from the spaces or create them to link two sets. There are also many operations that involve relations or sets (i.e. application, coalescing, transitive closure, etc.).

One of the possible use of ISL is for dependency analysis and it leads to many applications, such as loop optimizations [3]. In our case, we use ISL to compute a valid scheduling of the required kernel instances. We therefore define sets using ISL that corresponds to the kernel and data instances. This vision enables the compiler to compute dependencies directly on sets of kernel instances instead of individual instances and makes the complexity depend on the number of such sets instead of the number of instances in them. Eventually, we generate the code to schedule the execution as loops which largely reduces the size of the code compared to an approach where each kernel instance would be explicitly executed.

The dependency analysis relies on a four steps algorithm:

1) creation of the spaces and sets that represent the data and kernel domains;
2) creation of the relation between kernels and data instances;
3) creation of the order relations between kernels;
4) computation of the kernel instances that need to be executed to compute the targets.

First, the InKS compiler creates a space for each logical array and kernel depending on its dimension and a creates a set in each kernel space bounded by the kernel evolution domain. This is summarized in Algorithm 1.

Then, the compiler creates the input and output relations. For each kernel, we generate as many input relations as it requires values and as many output relations as it generates values. Input relations go from the data space to kernel space and opposite for output relations with constraints added to match the dependencies specified in InKS. An example of the constraints generated for the code presented in Listing 5 is given in Figure 1. For each relation, the compiler also creates a set that represents the data concerned by the relation. This is summarized in Algorithm 2.

The third step computes the order relation for kernel instances. A relation exists between two kernels \( k_1 \) and \( k_2 \) when the output of \( k_1 \) is required as input of \( k_2 \). Using ISL, the compiler computes the intersection of the input set for each kernel input relation with the output set of each kernel output relation. If the intersection \( \cap \) is not empty a dependency relation is generated as the composition of the inverse of the output relation and the input relation. This enables us to generate \( R_{map} \) as the union of all such relations that the orders all kernel instances.

The last step computes the kernel instances the program must actually execute. The compiler applies the inverse of the output relation to the target data set to determine the set of kernel instances that generate the target. Then it inverses the \( R_{map} \) order relation and computes its transitive closure. The resulting relation is applied to the set of kernel instances that generate the target to determine the complete set of instances to execute.

ISL propose a feature to generate a schedule that passes through all the points of a set by respecting an order relation. We use this feature to generate a schedule that iterates over...
the kernel instances that must be executed in a valid order. First, the compiler creates a schedule constraint using the kernel instances set and the order relation. Then it creates a schedule from this schedule constraint. ISL also supports the conversion of this schedule into an abstract syntactic tree (AST) that the compiler then transforms into valid C++ loop nests. The generated code is valid C++ that can be called from any other C++ code. For example for the 3D heat solver InKS semantic presented in Listing 4, the compiler generates the code presented in Listing 6.

Regarding logical arrays memory mapping and allocation, an efficient solution can not only take into account dependencies for allocation and deallocation of data. Allocating memory at the point granularity would lead to huge overheads as do irregular memory accesses. This aspect is still a work in progress in our compiler and the current implementation allocates all memory before starting to execute kernels and deallocates it after all executions. The mapping uses a distinct rectangle line major allocation for each logical array and makes no provision for memory reuse at all. It is implemented by instantiating a dedicated InKSArray class locally to let the C++ constructor/destructor mechanism handle memory management. This naive approach leads to extreme memory consumption that depends on the product of all dimensions sizes (including time) and offer very bad performance. It does however make it possible to test the other aspects of the compiler.

D. Manual specification of execution choices in InKS

Listing 7. Naive version using InKS manual approach

The manual specification of execution choices consist in implementing code equivalent to that of Listing 6 manually. The developer must provide classes that overload the parenthesis operator for the arrays and loop nests that schedule kernels execution in a valid order. The kernels can however be seen as black boxes whose understanding is not required. This code makes no provision for memory reuse at all. The mapping uses a distinct rectangle line major allocation for each logical array and deallocates it after all executions. The mapping uses a distinct rectangle line major allocation for each logical array and makes no provision for memory reuse at all. It is implemented by instantiating a dedicated InKSArray class locally to let the C++ constructor/destructor mechanism handle memory management. This naive approach leads to extreme memory consumption that depends on the product of all dimensions sizes (including time) and offer very bad performance. It does however make it possible to test the other aspects of the compiler.

The manual specification of execution choices consist in implementing code equivalent to that of Listing 6 manually. The developer must provide classes that overload the parenthesis operator for the arrays and loop nests that schedule kernels execution in a valid order. The kernels can however be seen as black boxes whose understanding is not required. This code can #include the semantic part of the code so as to inline function calls and offer similar performance as if it was written in a single file.

Listings 7 and 8 show examples of execution choices implementing the naive and cache oblivious optimizations respec-
void walk(IArray& Heat, int t0, int t1, int x0, int dx0, int xl, int dxl, int y0, int dy0, int yl, int dyl, int z0, int dz0, int zl, int dzl) {
  int dt=t1-t0;
  if(dt==1) { // simple case
    int x,y,z,t;
    for(t=t0; t<t1; t++) {
      for(x=x0+(t-t0)*dx0; x<x1+(t-t0)*dx1; x++) {
        for(y=y0+(t-t0)*dy0; y<y1+(t-t0)*dy1; y++) {
          for(z=z0+(t-t0)*dz0; z<z1+(t-t0)*dz1; z++) {
            Inner(Heat,x,y,z,t+1);
          }
        }
      }
    }
  } else if(dt>1) {
    int zm=(2*(z0+z1)+(2+dz0+dz1)*dt)/4;
    for(t=t0; t<t1; t++) {
      for(x=x0+(t-t0)*dx0; x<x1+(t-t0)*dx1; x++) {
        for(y=y0+(t-t0)*dy0; y<y1+(t-t0)*dy1; y++) {
          for(z=z0+(t-t0)*dz0; z<z1+(t-t0)*dz1; z++) {
            Inner(Heat,x,y,z,t+1);
          }
        }
      }
    }
  } else if(dt>0) {
    int ym=(2*(y0+y1)+(dy0+dy1)*dt)/4;
    for(t=t0; t<t1; t++) {
      for(x=x0+(t-t0)*dx0; x<x1+(t-t0)*dx1; x++) {
        for(y=y0+(t-t0)*dy0; y<y1+(t-t0)*dy1; y++) {
          for(z=z0+(t-t0)*dz0; z<z1+(t-t0)*dz1; z++) {
            Inner(Heat,x,y,z,t+1);
          }
        }
      }
    }
  } else {
    int zm=(z0+z1)/2;
    for(t=t0; t<t1; t++) {
      for(x=x0+(t-t0)*dx0; x<x1+(t-t0)*dx1; x++) {
        for(y=y0+(t-t0)*dy0; y<y1+(t-t0)*dy1; y++) {
          for(z=z0+(t-t0)*dz0; z<z1+(t-t0)*dz1; z++) {
            Inner(Heat,x,y,z,t+1);
          }
        }
      }
    }
  }
}

Listing 8. Cache oblivious version using InKS manual approach

tively based on the semantic in Listing 4. It demonstrates that while the classes used for array implementation must support the parenthesis operator, they can implement any additional required behavior. In Listing 6 the array supports a swap member function for double buffering while the IArray used in Listing 8 transparently implements the modulo on the time dimension. One could imagine other such tools to ease the implementation of well known optimization patterns. Such tools could even at least partly be generated using the semantic code using a compiler similar to that previously described.

This ends the presentation of the InKS model. The following section focuses on the evaluation of the approach.
time problem as discussed in Section IV.

Both the automatic and manually optimized versions of the code are standard C++ code that offer a C compatible interface. This code can easily be called from a larger program implemented in a language that supports the C calling convention. This is the case of most existing languages including C, C++, Fortran but even also python or Julia for example.

The specification of optimization choices is very close to their expression in C for example. The GNU complexity score is slightly lower due to the fact that the optimization code does no include the core of the kernels. The fact that an already existing language is used means that specialists of optimizations can reuse their knowledge in InKS. The existence of a reference code automatically generated from the semantic only also makes it possible to easily test the optimized code validity. In addition, in future work, information from the InKS annotations of the semantic could be used to automatically generate code representing common optimization patterns that the specialist could use.

Finally, when it comes to performance, the approach makes it possible to express optimizations that does not change the semantic. Optimizations such as changing the numerical scheme or changing the order of operations, potentially making the code generate different results is however out of the scope of this work. In the case of the 3D heat equation solver, both the naive and cache oblivious versions were trivial to implement and their performance match (or slightly outperform) the reference performance.

VI. CONCLUSION AND PERSPECTIVES

This report has presented InKS, a programming model intended to decouple optimization choices from semantics in high-performance simulation codes. InKS offers a language based on C++ and pragma annotations to express the semantics of simulations and supports automatic execution as well as manual specification of execution choices. Preliminary evaluations show that InKS manages to completely separate semantics from execution choices while enabling any optimization that can be expressed in C++ to be used. They also show that with manual choice of optimizations, InKS performs similarly to hand-tailored code in a more classical language while the automatic execution does not currently offer very good performance. One should however note that this automatic execution is only intended to test the semantic of the code and that the expression of both the semantic and optimization choices is typically no more complex than with an imperative language like C.

Many directions for future work have been identified in this report. The first such work we plan to tackle is the improvement of the memory management in the InKS automatic compiler so that the generated code does not differ by orders of magnitude in terms of performance from a typical implementation. A maybe more interesting feature is to support tools to ease the expression of optimizations. Many of the runtimes and languages identified in Sections III, such as OpenMP, Kokkos or StarPU could be wrapped to ease their use in optimization. By generating code that uses information from the InKS pragma annotations, a lot of tedious code could be automatically generated, thus easing the work of the developer.

REFERENCES


